

Bakó László

LISTA PUBLICAȚIILOR RELEVANTE

- 1) **Bakó László**, *Real-time classification of datasets with hardware embedded neuromorphic neural networks*, Briefings in Bioinformatics, Special Issue: Parallel and Ubiquitous Methods and Tools in Systems Biology: May 2010; Vol. 11, No. 3, p348-363, doi: 10.1093/bib/bbp066, Oxford University Press (**Impact Factor: 7.329**)
- 2) Fearghal Morgan, Arthur Beretta, Ian Gallivan, Joseph Clancy, Frédéric Rousseau, Roshan George, **László Bakó**, Frank Callaly, “RISC-V Online Tutor”, in Proceedings of the International Conference on Remote Engineering and Virtual Instrumentation (REV2021), Hong Kong (Virtual Conference), Feb 2021, pp 316-328.
- 3) Fearghal Morgan, Declan O’Loughlin, Jeremy Audiger, Yohan Boyer, Niall Timlin-Canning, Krzysztof Kępa, Ian Gallivan, Frank Callaly, **László Bakó**, “viciLogic 2.0: Online learning and prototyping of digital systems using PYNQ-Z1/-Z2 SoCs”, 29th International Symposium on Rapid System Prototyping (RSP), October 4-5, 2018, Torino, Italy.
- 4) A. Boyd, F. Callaly, D. Canavan, D. O’loughlin, J. Audiger, Y. Boyer, N. Timlin-Canning, **L. Bakó**, Sz. Hajdú, F. Morgan, “ICCapt: Online design capture and HDL generation, with PYNQ SoC prototyping in the cloud”, 29th Irish Signals and Systems Conference (ISSC 2018), Belfast, 21st - 22nd June, 2018.
- 5) L.F. Márton, S.T. Brassai, **L. Bakó**, L. Losonczi, Detrended Fluctuation Analysis of EEG Signals, Procedia Technology, Volume 12, 2014, Pages 125-132, ISSN 2212-0173, <https://doi.org/10.1016/j.protcy.2013.12.465>.
- 6) Róbert Moni, **László Bakó**, Szabolcs Hajdú, Fearghal Morgan and Sándor Tihamér Brassai, “Embedded Real-time Implementation of a Computational Efficient Optical Flow Extraction Method for Intelligent Robot Control Applications”, 24th Irish Conference on Artificial Intelligence and Cognitive Science, University College Dublin, September 20-21, 2016.
- 7) **Bakó, L.**, Brassai, S.T., Losonczi L. and Márton, L.F., “Evolving advanced neural networks on run-time reconfigurable digital hardware platform.”, In Proceedings of the 3rd International Workshop on Adaptive Self-Tuning Computing Systems (ADAPT’13 - January 21-23, 2013, Berlin, Germany), ACM, New York, NY, USA, 2013, Article 3, <http://doi.acm.org/10.1145/2484904.2484907>
- 8) **L. Bakó**, F. Morgan, Sz. Hajdú, S.-T. Brassai, R. Moni, C. Enăchescu, Development and Embedded Implementations of a Hardware-Efficient Optical Flow Detection Method, Acta Universitatis Sapientiae, Electrical and Mechanical Engineering, 6 (2014) 5–19.
- 9) **Bakó, L.**; Brassai, S.; Kolcsar, A.; Losonczi, L.; Marton, L., „Neuromorphic Neural Network Parallelization on CUDA Compatible GPU for EEG Signal Classification”, IEEE Computer Modeling and Simulation (EMS), 2012 Sixth UKSim/AMSS European Symposium on, Valetta, Malta, 14-16 Nov. 2012, pp.359-364, ISBN 978-1-4673-4977-2, DOI 10.1109/EMS.2012.87.
- 10) **Bakó L.**, Brassai S.T., Hardware spiking neural networks: parallel implementations using FPGAs, Proceedings of the 8th WSEAS Int. Conference on Automatic Control, Modeling and Simulation, Prague, Czech Republic, March 12-14, 2006 (pp. 261-266), ISBN 960-8457-42-4, ISSN 1790-5117
- 11) Brassai, S.T., **Bakó L.**, “Visual Trajectory Control of a Mobile Robot Using FPGA Implemented Neural Network”, Pollack Periodica, An International Journal for Engineering and Information Sciences, Pollack.4.2009.3.12, Vol. 4, No. 3, pp. 129–142 (December 2009), Akadémiai Kiadó, Budapest, Hungary, ISSN 1788-3911, SJR 0,031.
- 12) **Bakó, L.**, „Real-time clustering of datasets with hardware embedded neuromorphic neural networks”, HiBi 2009 (High performance computational systems Biology) Workshop, COSBi (Microsoft Research - University of Trento Centre for Computational and Systems Biology), Trento, Italy, October 14-16, 2009, Published by IEEE Computer Society, ISBN 978-0-7695-3809-9, pp 13-22, DOI: 10.1109/HiBi.2009.24.